

[54] **METHOD AND CIRCUITRY FOR DUAL PANEL DISPLAYS**
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 [52] **U.S. Cl.** **364/518; 340/716; 340/723; 364/521**
 [58] **Field of Search** **364/518, 521; 340/721, 340/723, 724, 725, 726, 709, 716, 752, 756; 358/240**

[57] **ABSTRACT**

A data processing system includes a video controller and a flat panel display system. The flat panel display system includes first and second flat panel displays adapted so as to appear to be essentially a single display. The video controller includes a first and second address generator for generating first and second address information and a counter for counting vertical sync position to identify alternating first and second display frames. The controller outputs the first address information to drive the first display and the second address information to drive the second display during the first display frames and outputs the second address information to drive the first display and the first address information to drive the second display during the second display frames. The displays are identical and the address generators are identical. The use of dual displays with dual, flip-flopping address generators is more advantageous than using a single address generator to drive two display panels.

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