

[54] **GRAPHICS DISPLAY CONTROLLER  
 EQUIPPED WITH BOUNDARY SEARCHING  
 CIRCUIT**

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 [21] **Appl. No.:** 161,690  
 [22] **Filed:** Feb. 29, 1988

[30] **Foreign Application Priority Data**  
 Feb. 27, 1987 [JP] Japan ..... 62-44840

[51] **Int. Cl.<sup>5</sup>** ..... G09G 1/00  
 [52] **U.S. Cl.** ..... 340/723; 340/747;  
 340/799; 382/22; 382/48  
 [58] **Field of Search** ..... 340/703, 720, 723, 724,  
 340/728, 747, 798, 799; 358/96; 382/22, 48

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[57] **ABSTRACT**

A graphics display controller equipped with a boundary search circuit is disclosed. There is provided a mask information generator generating mask information consisting of a plurality of bits in response to control data, at least one bit of the mask information taking non-mask data and the remaining bits thereof taking mask data. Each bit data of the mask information is supplied to the associated one of a plurality of mask gates along with the associated bit data of display data of one word read from a display memory. Each of the mask gates outputs the associated bit data of the display data when the associated mask information bit data is the non-mask data. The output data of the mask gates are supplied to a position information generator wherein the position information relative to a position of a bit taking a predetermined logic level is generated.

**8 Claims, 9 Drawing Sheets**

