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Oguchi

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[54]	INFORMATION PROCESSING APPARATUS HAVING A MASK FUNCTION				
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[57] ABSTRACT

An information processing apparatus having a mask operation includes a first circuit generating a first mask control signal indicating a start bit position of a data to be processed and a second circuit generating a second mask control signal indicating an end bit position of that data. The first mask control signal and the second mask control signal are applied to a gate circuit by which a non-mask signal is produced for a bit or bits from the start bit position to the end bit position and a mask signal is produced for the other bit or bits.

9 Claims, 5 Drawing Sheets

