

# United States Patent [19] Oguchi

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- [54] DISPLAY CONTROLLING APPARATUS
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### Related U.S. Application Data

- [63] Continuation of Ser. No. 304,583, Sep. 22, 1981, abandoned.

### Foreign Application Priority Data

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[51] Int. Cl.<sup>3</sup> ..... G09G 1/16

[52] U.S. Cl. .... 340/726; 340/723;  
340/750

[58] Field of Search ..... 340/723, 726, 744, 748,  
340/749, 750

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### [57] ABSTRACT

A display control system has a memory for storing display information and a memory access circuit for reading display information out of this memory. This memory access circuit includes a first circuit in which a memory address is set, a second circuit for sequentially varying the memory address by a predetermined value, and a third circuit for adding to the memory address a preset value, which is different from the predetermined value. A control circuit gives a designation of the addresses to the memory, as a result of the cooperation of the second circuit and the third circuit. The control circuit can be achieved so that display information is read while a memory address may be varied by at least two different means (the second and third circuits above). Thus, it becomes possible to selectively designate a part of a memory region and to display the information of the selected memory region.

4 Claims, 9 Drawing Figures

