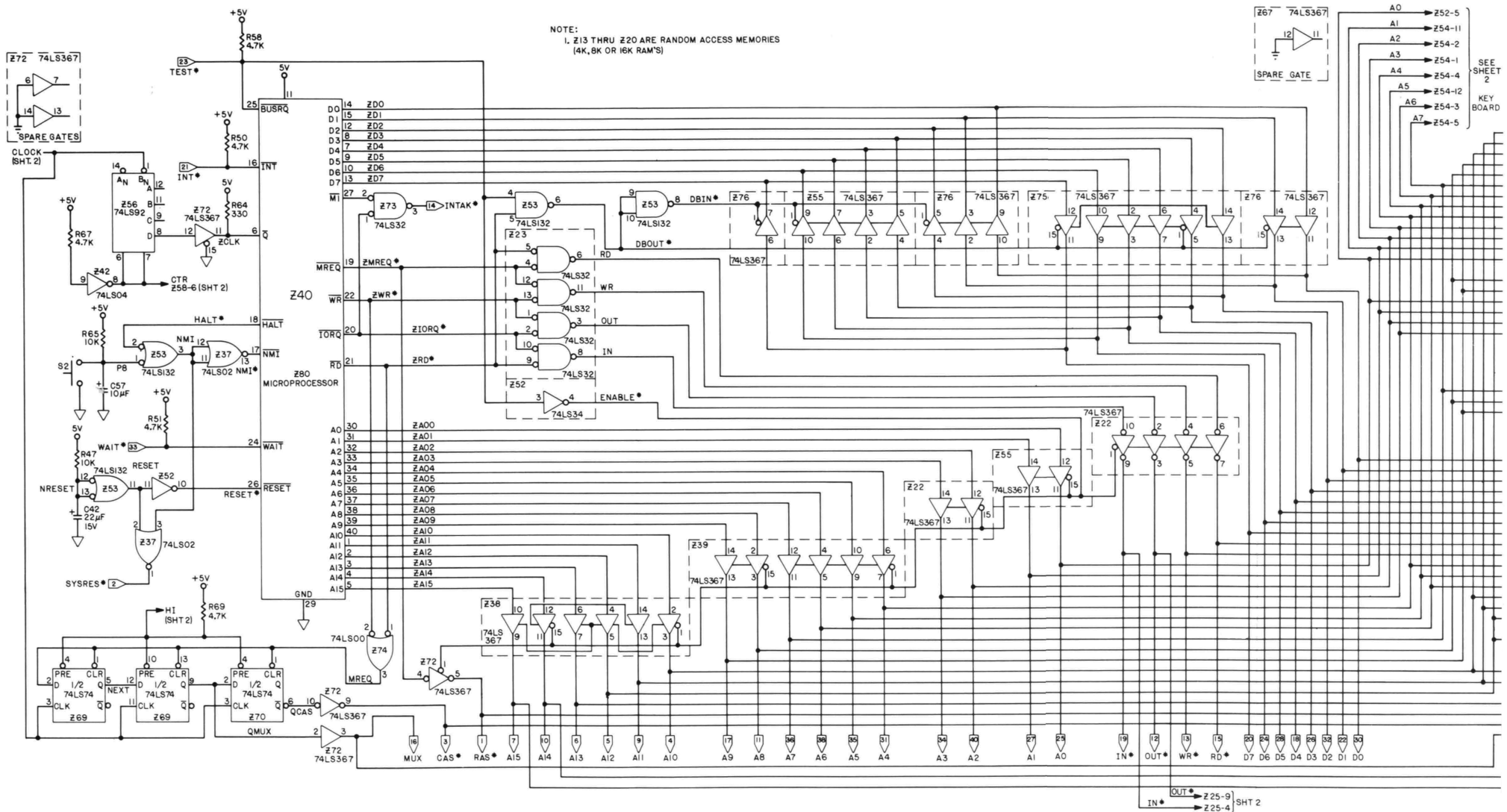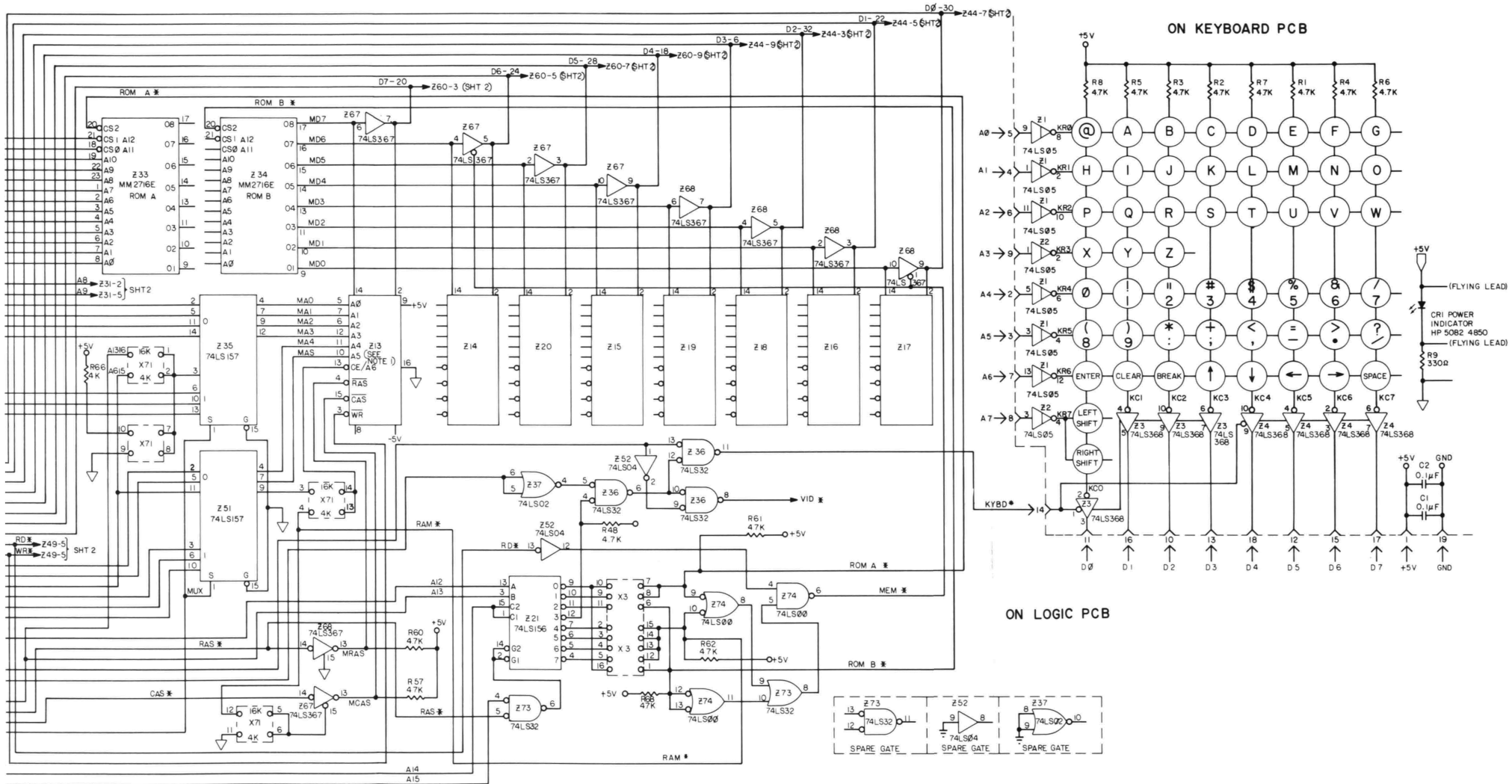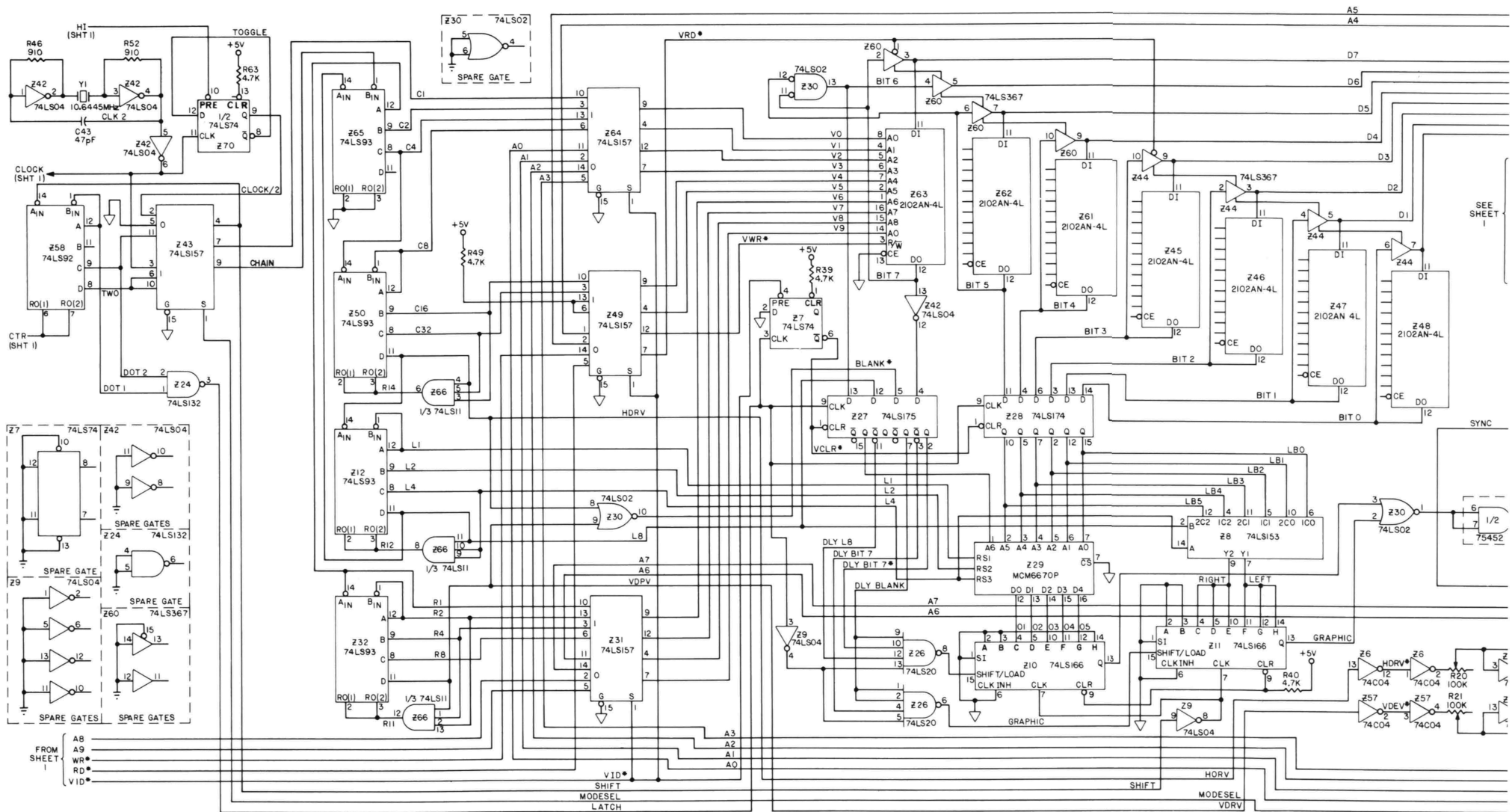TRS-80 Main Keyboard Block Diagram
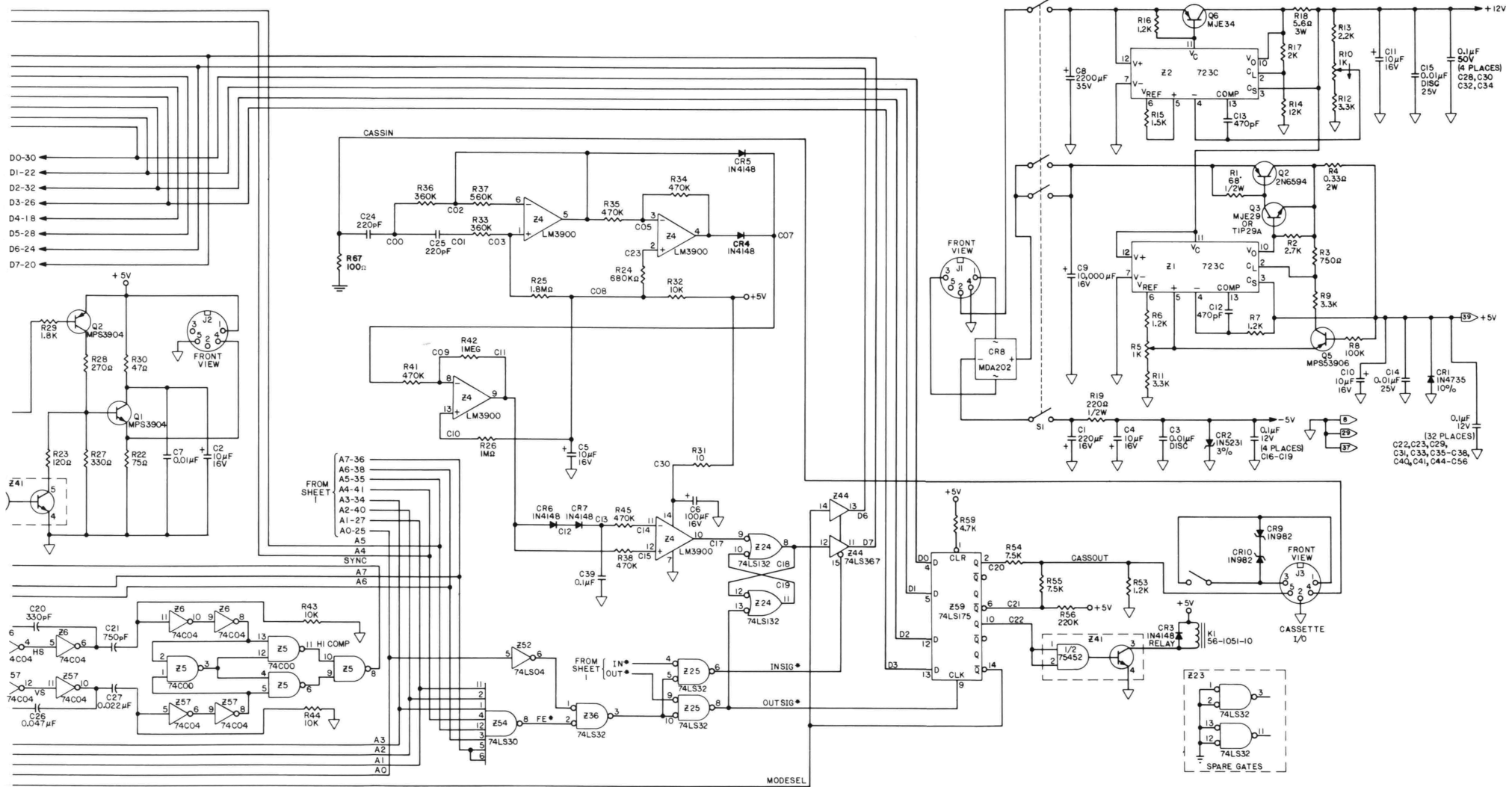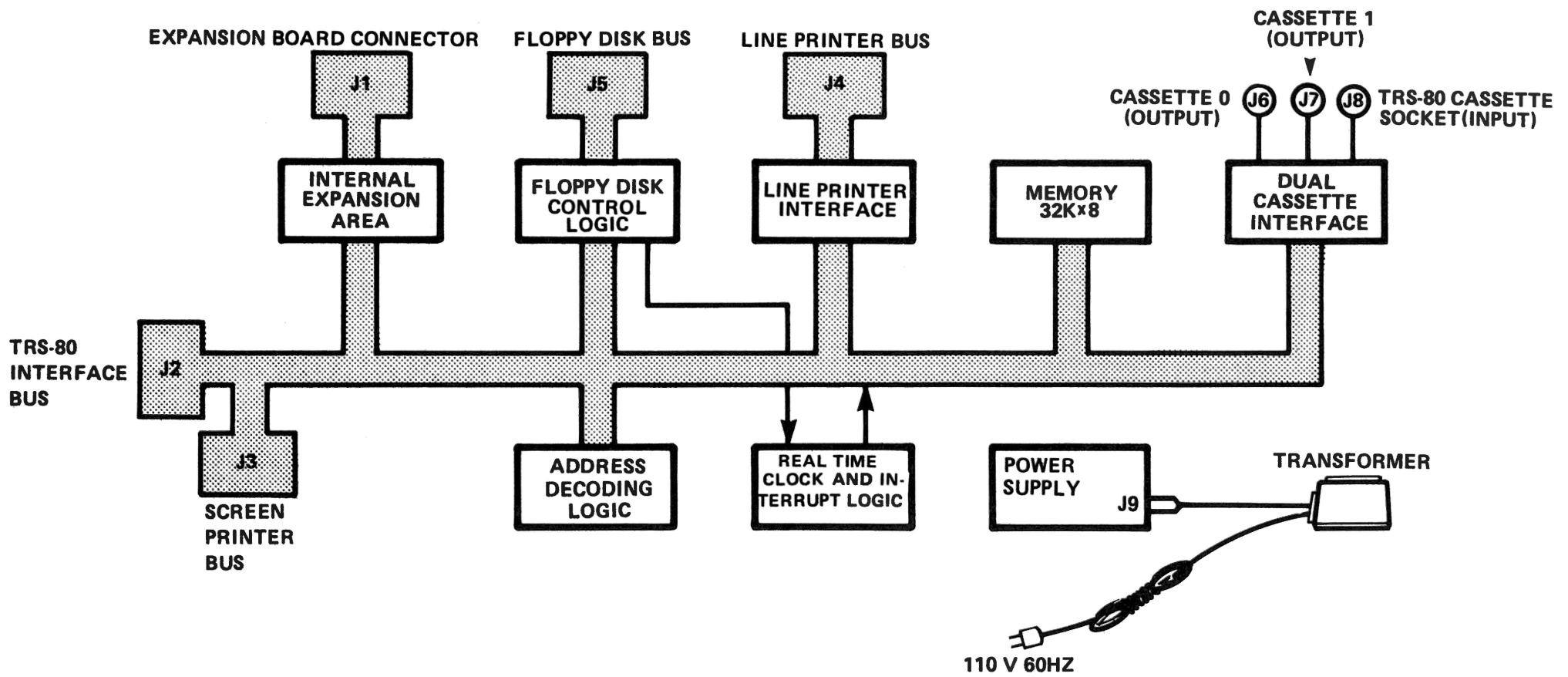
TRS-80 Main Keyboard Sheet 1 (Left)

TRS-80 Main Keyboard Sheet 1 (Right)

TRS-80 Main Keyboard Sheet 2 (Left)

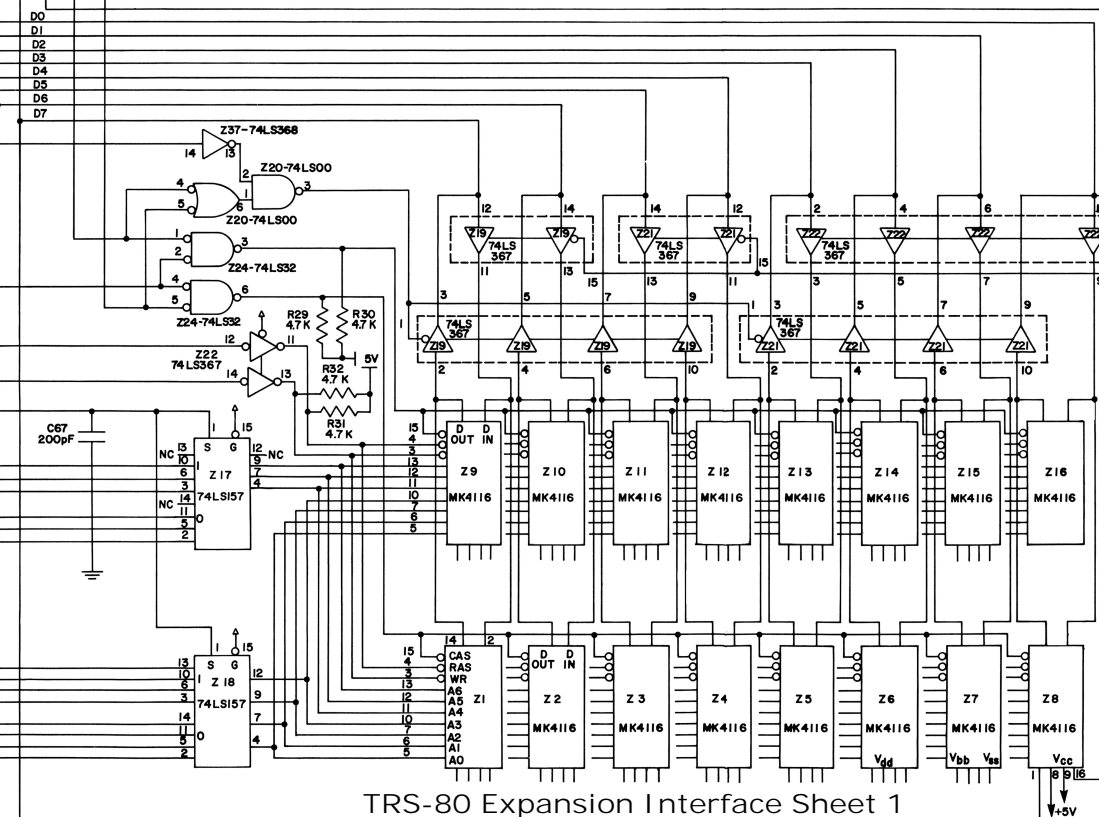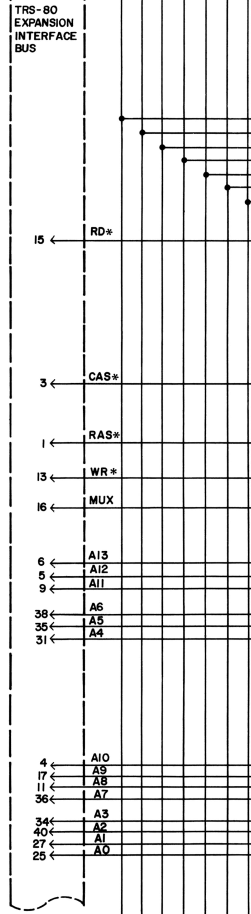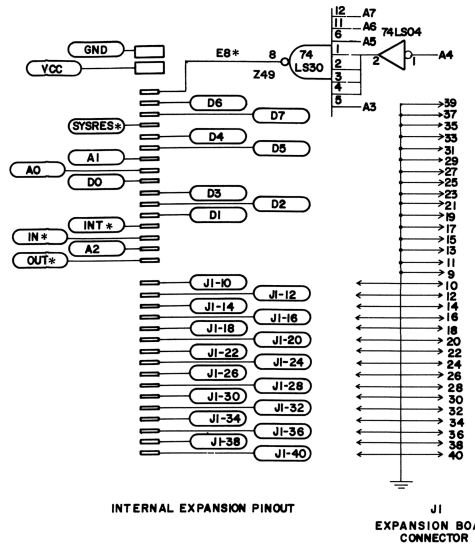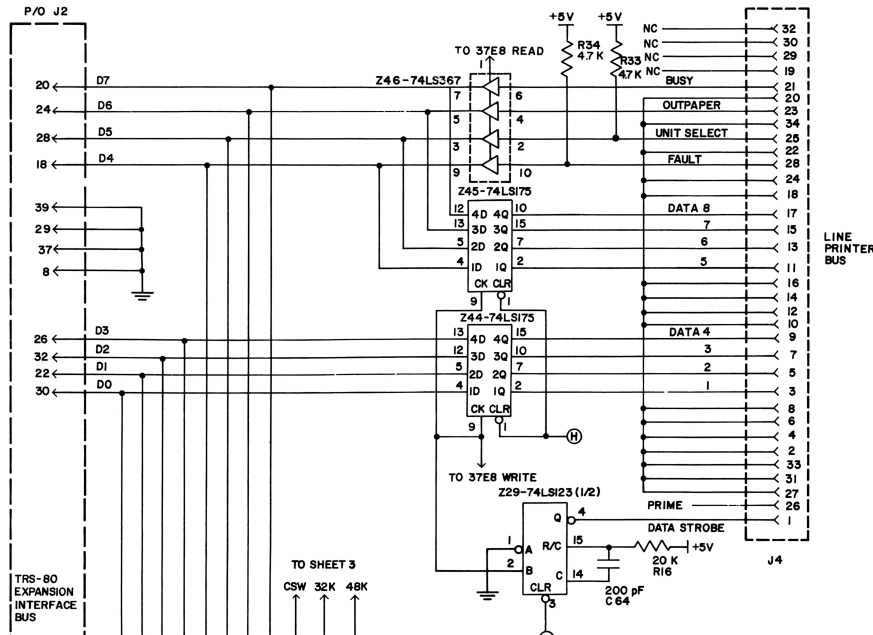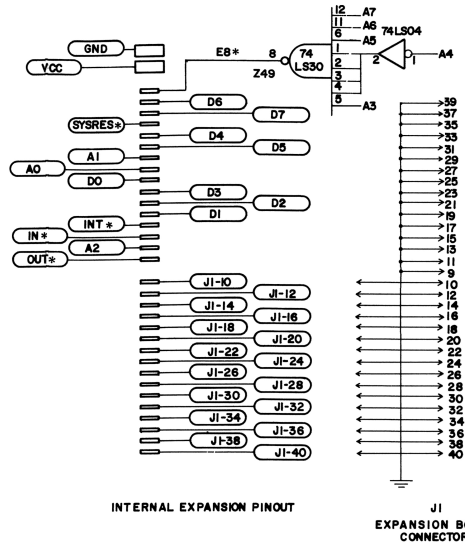TRS-80 Main Keyboard Sheet 2 (Right)

EXPANSION BOARD CONNECTOR

FLOPPY DISK BUS

LINE PRINTER BUS

CASSETTE 1 (OUTPUT)

CASSETTE 0 (OUTPUT)

TRS-80 CASSETTE SOCKET(INPUT)

J1

J5

J4

J6 J7 J8

INTERNAL EXPANSION AREA

FLOPPY DISK CONTROL LOGIC

LINE PRINTER INTERFACE

MEMORY 32K×8

DUAL CASSETTE INTERFACE

TRS-80 INTERFACE BUS

J2

J3

ADDRESS DECODING LOGIC

REAL TIME CLOCK AND IN-TERRUPT LOGIC

POWER SUPPLY J9

TRANSFORMER

SCREEN PRINTER BUS

110 V 60HZ

TRS-80 Expansion Interface Block Diagram

TRS-80 Expansion Interface Sheet 1

TRS-80 Expansion Interface Sheet 2

TRS-80 EXPANSION INTERFACE
SERVICE MANUAL

INTERNAL EXPANSION PINOUT

J1
EXPANSION BOARD
CONNECTOR

FRONT VIEW
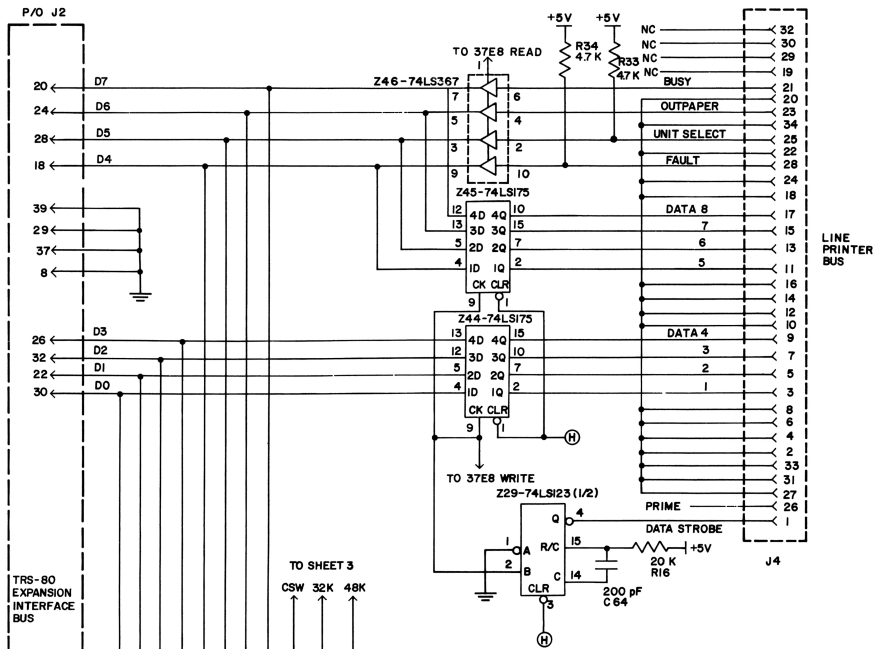
J6
CASSETTE
0
(OUTPUT)

J7
CASSETTE
1
(OUTPUT)

J8

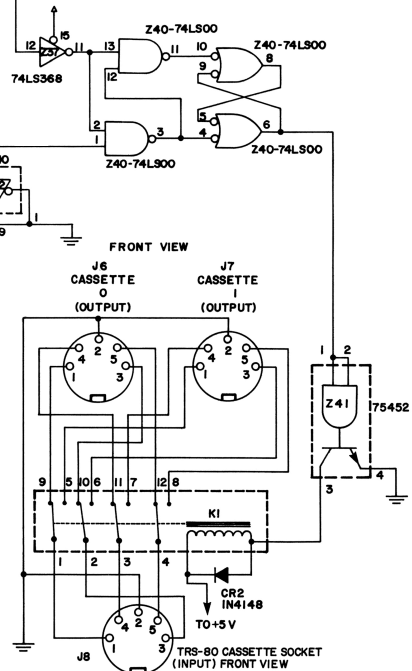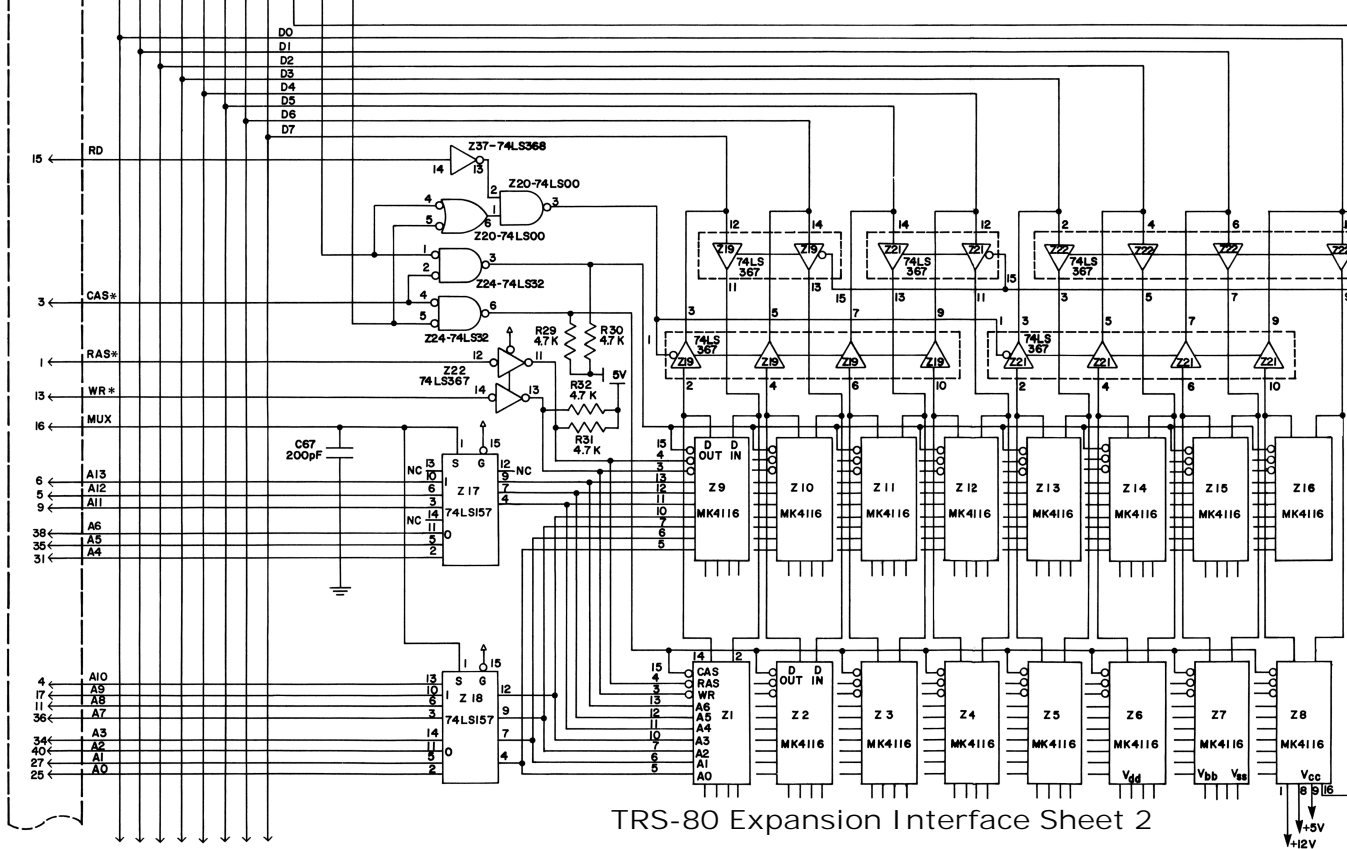TRS-80 CASSETTE SOCKET
(INPUT) FRONT VIEW
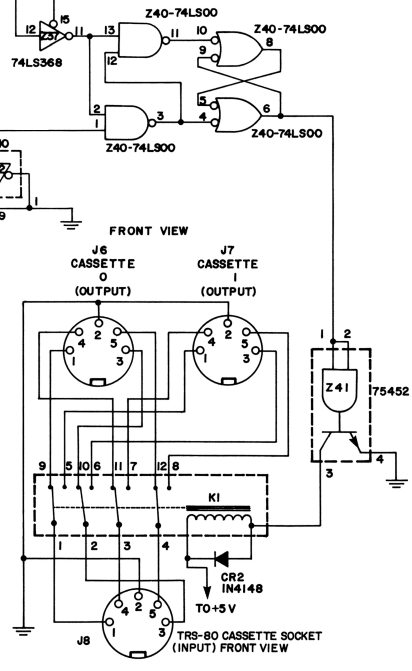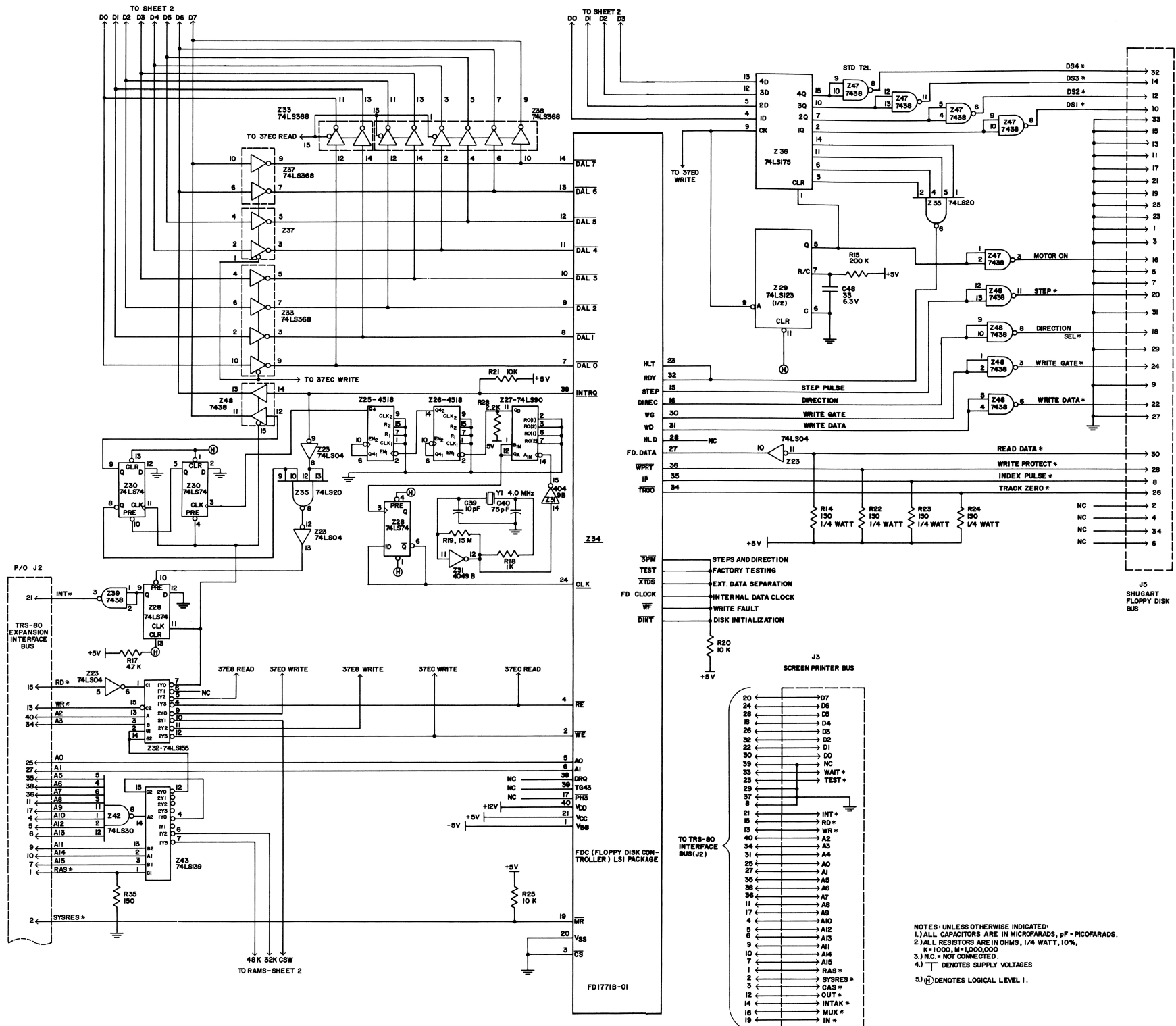
NOTES: UNLESS OTHERWISE INDICATED:
1.) ALL CAPACITORS ARE IN PICOFARADS, 10%.
2.) ALL RESISTORS ARE IN OHMS, 1/4 WATT, 10%. K=1000.
3.) ALL RAMS(MK 4116) ARE IN 16 PIN SOCKETS.
4.) PINS 1,3,4,5,6,7,8,9,10,11,12,13 AND 16 OF ALL
RAMS ARE TIED IN PARALLEL.
5.) (H) DENOTES LOGICAL LEVEL 1.

DATA LINES D0-D7 TO SHEET 3

TRS-80 Expansion Interface Sheet 3